Prasad V. Potluri Siddhartha Institute of Technology (Autonomous), Kanuru, Vijayawada-520007

Department of Electronics and Communication Engineering

Bharat Electronics Limited (BEL), Hyderabad Academic Year: 2023-24

S. No.	Roll Number	Name of the student	Title of the internship	Duration
1	22505A0409	S. Narayana · Swami	Design of Embedded system in the FPGA	June'23 To July'23 (8 weeks)
2	22505A0413	Yallanki Ashok	Design of Embedded system in the FPGA	June'23 To July'23 (8 weeks)
3	22505A0402	Guttula Jnana Teja	Design of Embedded system in the FPGA	June'23 To July'23 (8 weeks)

(Dr.C.Subba Rao) - '
Prof. & HOD, ECE

HEAD

Electronics & Communication Engg.Dept.
PRASAD V.POTLURI
SIDDHARTHA INSTITUTE OF TECHNOLOGY
KANURU, VIJAYAWADA-520 00%.

भू थण्ड २० हा रहि आरत इलेक्ट्रॉनिक्स BHARAT ELECTRONICS

> भारत इलेक्ट्रॉनिक्स लिमिटेड (भारत सरकार का एक उद्यम, रक्षा मंत्रालय) आई.ई.नाचारम, हैदराबाद - 500076, भारत

Bharat Electronics Limited

(A. Govt. of India Enterprise, Ministry of Defence) I.E. Nacharam, Hyderabad - 500076, India বুংশাৰ Phone: +91 40 27194700, Extn:.....

फेक्स Fax: +91 40 27171406

Date: 07-08-2023

CERTIFICATE

This is to certify that Mr. S. Narayana Swami (Roll No:22505A0409), pursuing B.Tech (ECE) II/IV from PVP Siddhartha Institute of Technology, Vijayawada has successfully completed the Internship Project entitled "Design of Embedded system in the FPGA" during the period June'23 to July'23 (8 weeks), under the guidance and supervision of undersigned. During his stay in our company, the student was sincere and hard working.

For BHARAT ELECTRONICS LIMITED

Srinu A

Manager

Naval Systems-D&E



भारत इलेक्ट्रॉनिक्स लिमिटेड (भारत सरकार का एक उद्यम, रक्षा मंत्रालय) आई.ई.नाचारम, हैदराबाद - 500076, भारत Bharat Electronics Limited

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फेक्स Fax: +91 40 27171406

Date: 07-08-2023

CERTIFICATE

This is to certify that Mr. Yallanki Ashok (Roll No:22505A0413), pursing B.Tech (ECE) II/IV from PVP Siddhartha Institute of Technology, Vijayawada has successfully completed the Internship Project entitled "Design of Embedded system in the FPGA" during the period June'23 to July'23 (8 weeks), under the guidance and supervision of undersigned. During his stay in our company, the student was sincere and hard working.

For BHARAT ELECTRONICS LIMITED

A. Grinu A

Manager

Naval Systems-D&E